

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	218	in-circuit with (processor or microprocessor or micro-processor) with emulator	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/30 19:41
L2	0	1 same adjust\$6 same speed	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/30 19:41
L3	1	1 same dynamic\$6 same speed	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/30 19:41
L4	1881	(711/103).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/30 19:41
L5	1679	(711/167).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/30 19:41